

# Labview Core Fpga Manual

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Figura 38 El patrón de diseño Productor/Consumidor de LabVIEW es usado para aumentar el rendimiento de las aplicaciones que requieren tareas paralelas Figura 39 El código de LabVIEW FPGA con paralelismo genera caminos independientes en el chip de la FPGA Figura 310 Formula Node usa sintaxis similar a C para representar expresiones

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